**WEEK6 PROGRAM**

**16 Bit Program Counter**

**Students have to complete pc.v**

Step1) **iverilog -o test\_pc lib.v pc.v tb\_pc.v**

If the compilation went OK, you won't see any output. What this does is create a file called test\_pc that we can feed to the simulator.

Step2) **vvp test\_pc**

You can observe output on the console

Step3) **gtkwave tb\_pc.vcd**

Output waveform will be observed.